

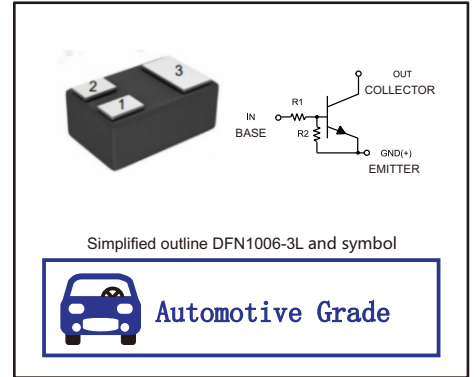


AT-JDTD143ZDC

NPN Digital Transistor

PINNING

PIN	DESCRIPTION
1	BASE
2	EMITTER
3	COLLECTOR



FEATURES

- Epitaxial Planar Die Construction
- Built-In Biasing Resistors
- Simplifies Circuit Design
- Reduces Board Space and Component Count
- Qualified to AEC-Q101 Standards for High Reliability

DEVICE RESISTOR VALUES

Type	R1(KΩ)	R2(KΩ)
JDTD143ZDC	4.7	47

Absolute Maximum Ratings And Characteristics (Ta = 25°C)

Parameter	Symbol	Value	Unit
Collector-Base Voltage	V _{CB0}	50	V
Collector-Emitter Voltage	V _{CEO}	50	V
Collector Current	I _c	100	mA
Total PowerDissipation @ T _A = 25°C (NOTE 1)	P _D	250	mW

NOTE: (1)Reflow soldering is the only recommended soldering method.
Device mounted on an FR4PCB with 70 μm copper strip line, standard footprint

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance – Junction-to-Ambient(Note 2)	R _{θJA}	500	°C/W
Operating and Storage Temperature Range	T _J , T _{stg}	-65 to +150	°C
Maximum Temperature for Soldering Purposes, Time in Solder Bath	T _L	260 10	°C Sec

NOTE: (2)Reflow soldering is the only recommended soldering method.
Device mounted on an FR4PCB with 70 μm copper strip line, standard footprint

ELECTRICAL CHARACTERISTICS (TA = 25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Collector-Base CutoffCurrent	I _{cBO}	V _{CB} =50V, I _E =0			100	nA
Collector-Emitter Cut off Current	I _{CEO}	V _{CE} =50V, I _B =0			1	uA
Emitter-Base Cut off Current	I _{EB0}	V _{EB} =5.0V, I _C =0			0.17	mA
Collector-Base Breakdown Voltage	V _{(BR)CBO}	I _C =10uA, I _E =0	50			V
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	I _C =2.0mA, I _B =0	50			V
ON CHARACTERISTICS						
DC Current Gain	h _{FE}	V _{CE} =10V, I _C =5mA	100			
Collector-Emitter Saturation Voltage	V _{CE(sat)}	I _C =5mA, I _B =0.25mA			0.1	V



Parameter	Symbol	Conditions	Min	Typ	Max	Unit
ON CHARACTERISTICS						
Input Voltage(on)	V_{IL}	$V_{CE}=0.3V, I_C=5mA$	1.3	0.9		V
Input Voltage(off)	V_{IH}	$V_{CE}=5.0V, I_C=100\mu A$		0.6		V
Input Resistor	R_1		3.3		6.1	k Ω
Input Resistor	R_2				58.3	k Ω
Resistor Ratio	R_2/R_1		8		12	
Collector Capacitance	C_C	$V_{CB}=10V, I_E=0A, f=1MHz$			2.5	pF
Transition Frequency	f_T	$V_{CE}=5V, I_C=10mA, f=100MHz$		230		Mhz

Fig 1. Transient thermal impedance from junction to ambient as a function of pulse duration for JDTD143ZDC; typical values

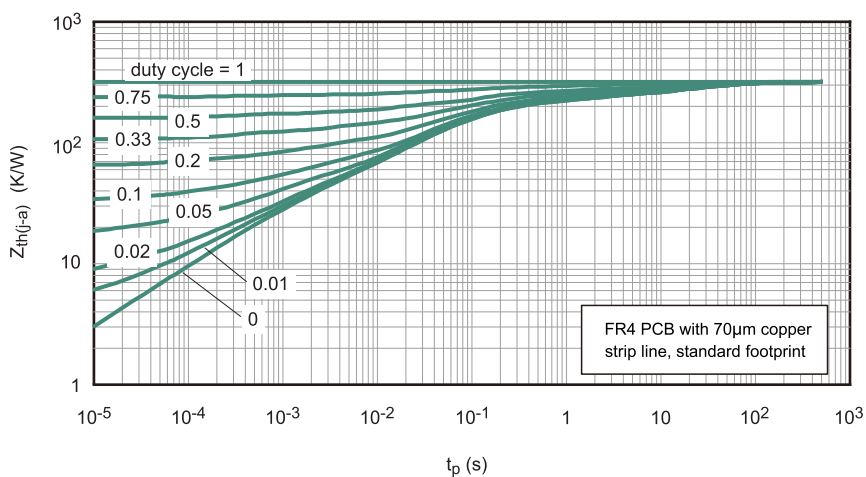


Fig 2. Power derating curves

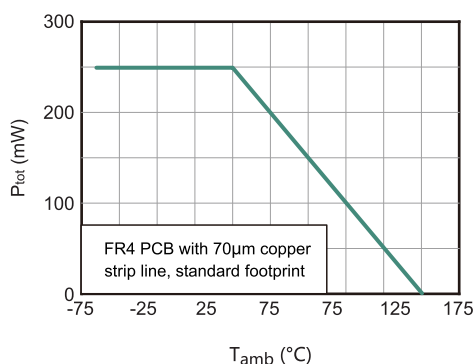


Fig 3. DC current gain as a function of collector current; typical values

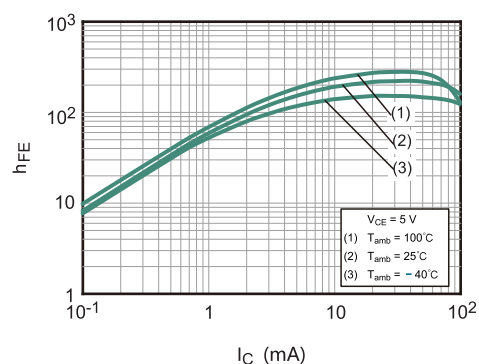




Fig 4. Collector-emitter saturation voltage as a function of collector current; typical values

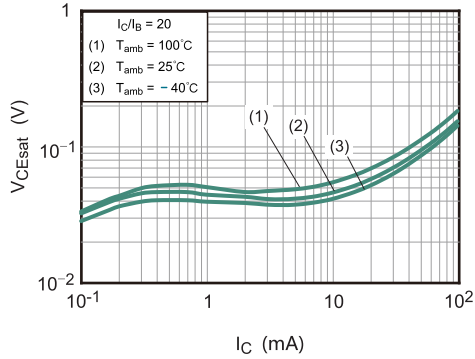


Fig 5. On-state input voltage as a function of collector current; typical values

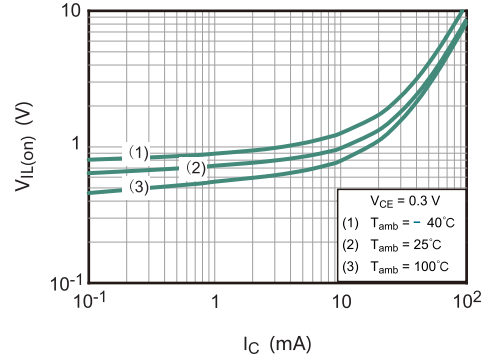


Fig 6. Off-state input voltage as a function of collector current; typical values

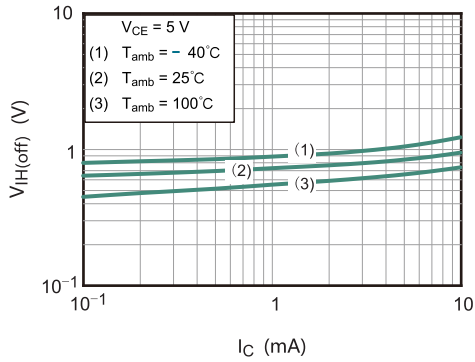
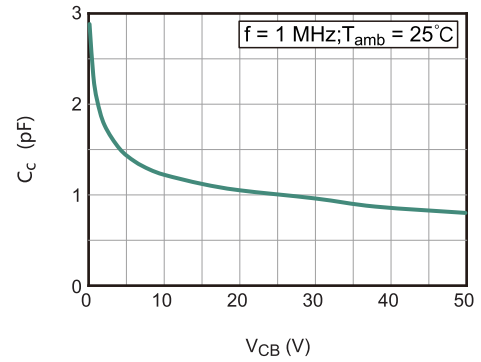
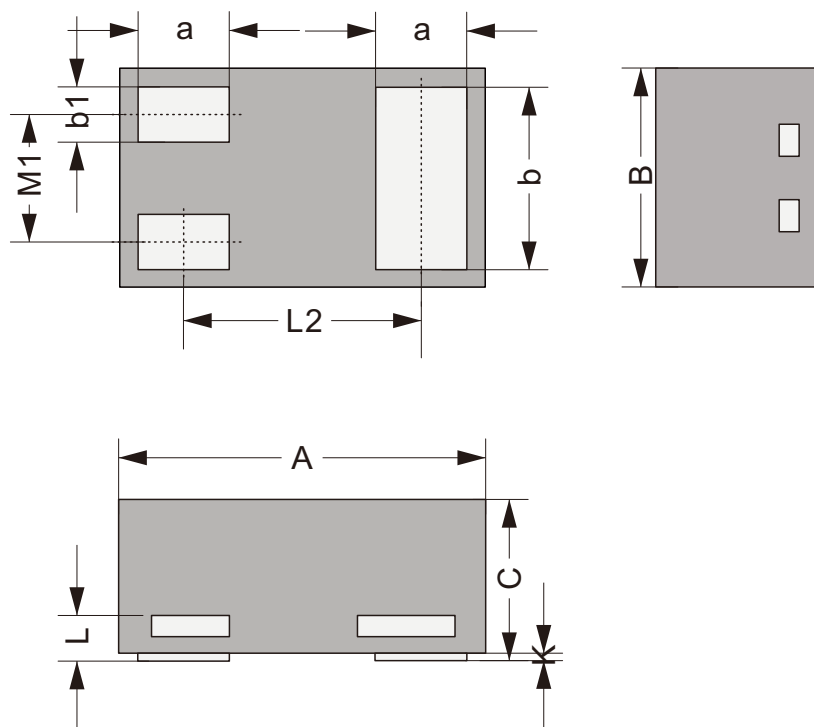


Fig 7. Collector capacitance as a function of collector-base voltage; typical values





DFN1006-3L Package Outline Dimensions



DFN1006-3L mechanical data

UNIT		A	B	C	L2	M1	L	a	b	b1	k
mm	max	1.05	0.65	0.55	0.65	0.35	0.152	0.30	0.55	0.20	0.03
	min	0.95	0.55	0.45	REF	REF	REF	0.22	0.47	0.12	
mil	max	41.34	25.59	21.65	25.59	13.78	5.98	11.81	21.65	7.87	1.18
	min	37.40	21.65	17.72	REF	REF	REF	8.66	18.50	4.72	

Marking

Type number	Marking code
JDTD143ZDC	43Z



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